

IN THE SPECIFICATION

Please replace the following paragraphs:

Page 3, paragraph [0008] through page 4, paragraph [0014].

[0008] FIG. 1 is a block diagram illustrating an embodiment of the disclosed information handling system (IHS).

[0009] FIG. 2 is a representation illustrating an embodiment of the zero voltage switching power supply in the IHS of FIG. 1.

[0010] FIG. 3A is a representation illustrating an embodiment of a conventional inductor with a core having a constant gap distance.

[0011] FIG. 3B – 3D are representations illustrating embodiments of inductors with a cores having non-constant gap distances.

[0012] FIG. 4A is a representation illustrating an embodiment of a conventional inductor with a core having a constant gap distance.

[0013] FIG. 4B – 4G are representations illustrating embodiments of inductors with cores having non-constant gap distances.

[0014] FIG. 5A – 5E are representations illustrating embodiments of inductors with cores having non-constant gap distances.

Page 7, paragraph [0021].

[0021] Switches 230 and 235 are alternately opened and closed in complementary fashion while supply 200 operates. Switching signals from driver 280 are provided to switches 230 and 235 as part of the switching process. The output voltage VO is compared with a desired output reference voltage, VREF, by error comparator 285. An error signal is generated at the output of error comparator 285 which is coupled to a voltage controlled oscillator (VCO) 290. The error signal is an indication of how far off the actual output voltage, VO, is from the desired output reference voltage, VREF. Accordingly, the frequency of VCO 290 is varied to control the frequency of the driver signal pulses used in switching switches 230 and 235 on and off. The frequency of the driver signal is varied until VO equals VREF.

Page 8, paragraph [0023].

[0023] Thus far, circuit operation has been described during the time that switch 230 is on. Switch 230 is now turned off by the switching signal from driver 280. However, switch 235B is not immediately turned on. At this point it is noted that switches 230 and 235 include parasitic body diodes 230A and 230B, respectively. These body diodes are parasitics which are inherent in the fabrication of MOS FET switches. If bipolar transistors are used as switches 230 and 235, then discrete diodes are added to these switches ~~since~~ because bipolar transistors do not have intrinsic parasitic body diodes. When switch 230 is turned off while current was flowing in inductor 250 in the direction indicated for current I1, the field of the inductor is built up and, due to Lenz's law, inductor current will continue to flow in the same direction. Inductor 250 becomes a current source. The energy from inductor 250 assists transistor 230 in turning off and also assists transistor 235's intrinsic body diode 235B in turning on. After switch 230 has transitioned losslessly as described above and switch 235's body diode 235B is turned on, switch 235 is now

turned on by the switching signal from driver 280. Turning switch 235 on causes inductor 250 to discharge with its current eventually changing direction and flowing toward node 240 as shown by current I1. Switch 235 then turns off under the instruction of the driver signal from drive 280. This transition again occurs losslessly due to body diode action. The driver signal from driver 280 then turns switch 230 back on and the process repeats. Zero voltage switching saves a substantial amount of energy by conserving energy that would otherwise be consumed during transistor switching.